

The diagram illustrates a multi-layered structure with four horizontal layers labeled 1, 2, 3, and 4 from bottom to top. Layer 1 contains input nodes T_{Li} , T_{Pi} , and T_{Ri} . Layer 2 contains intermediate nodes E_{Li} and E_{Ri} . Layer 3 contains nodes C_{Li} and C_{Ri} . Layer 4 contains output nodes Y_{Li} and Y_{Ri} . The units are grouped into blocks A_1 , A_2 , ..., A_i . Each block A_i contains a set of nodes connected by vertical lines. A dashed line separates layer 3 from layer 4. A bracket on the right groups the output nodes Y_{Li} and Y_{Ri} under the label Y_i .

FIG.2

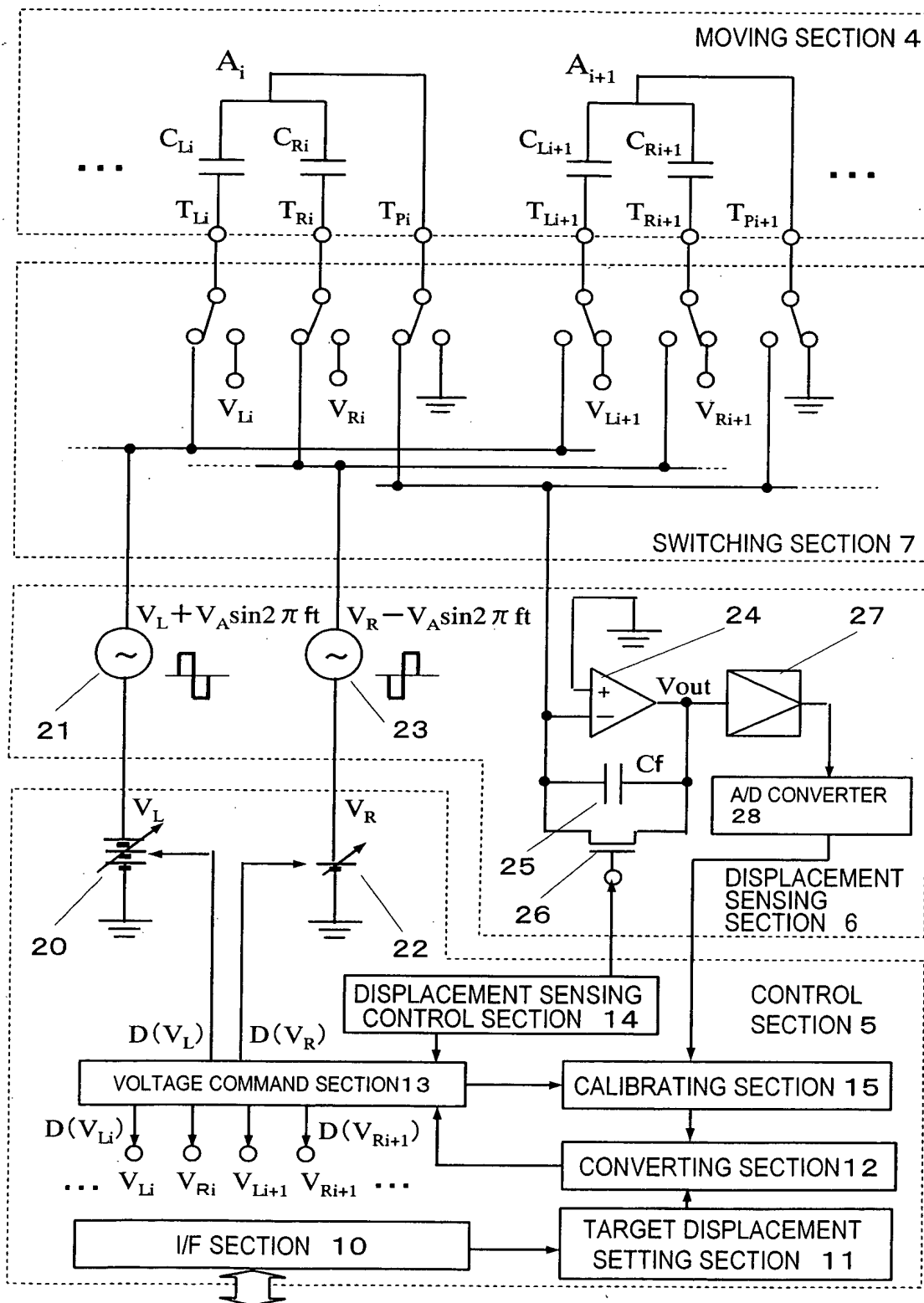


FIG. 3

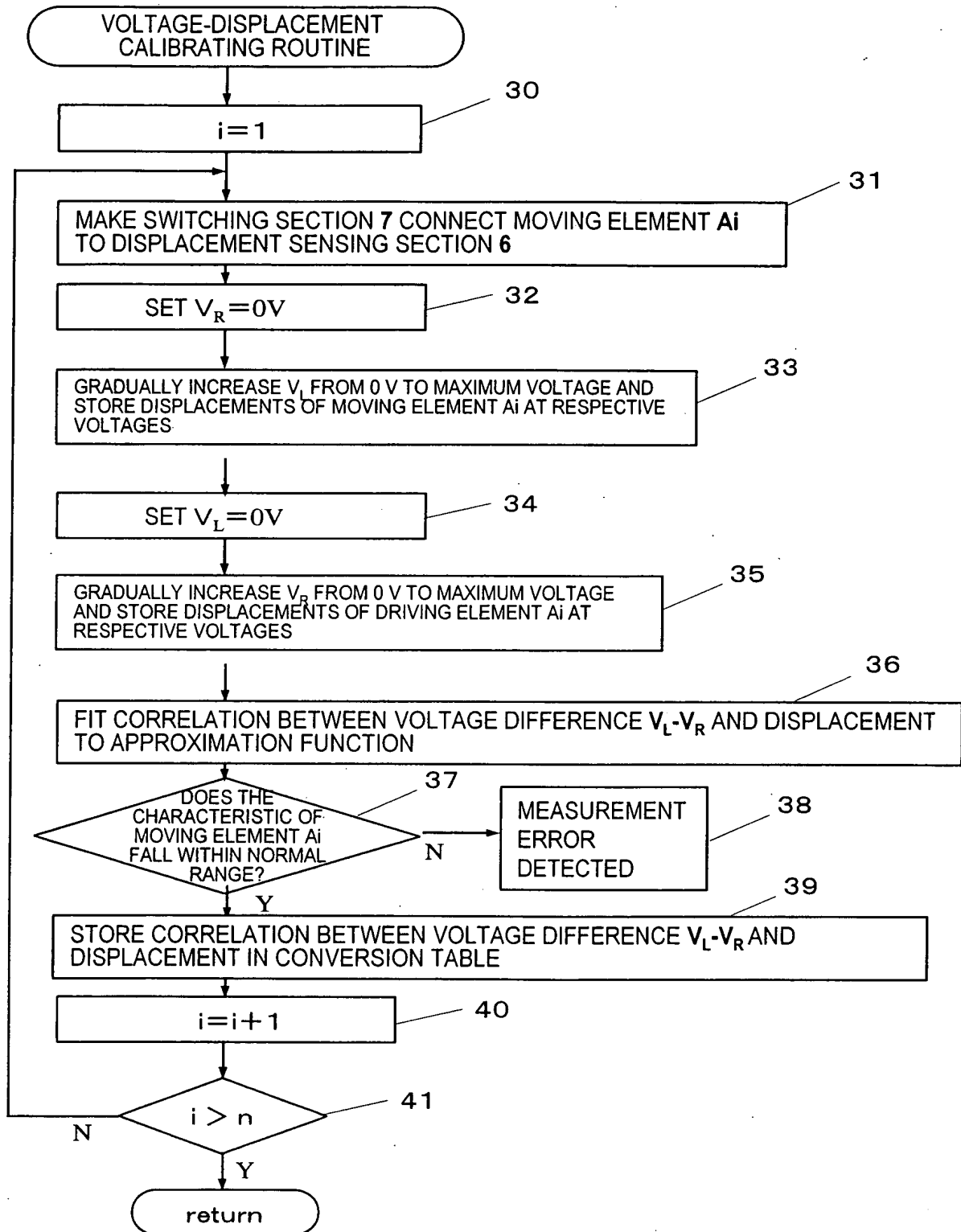


FIG. 4

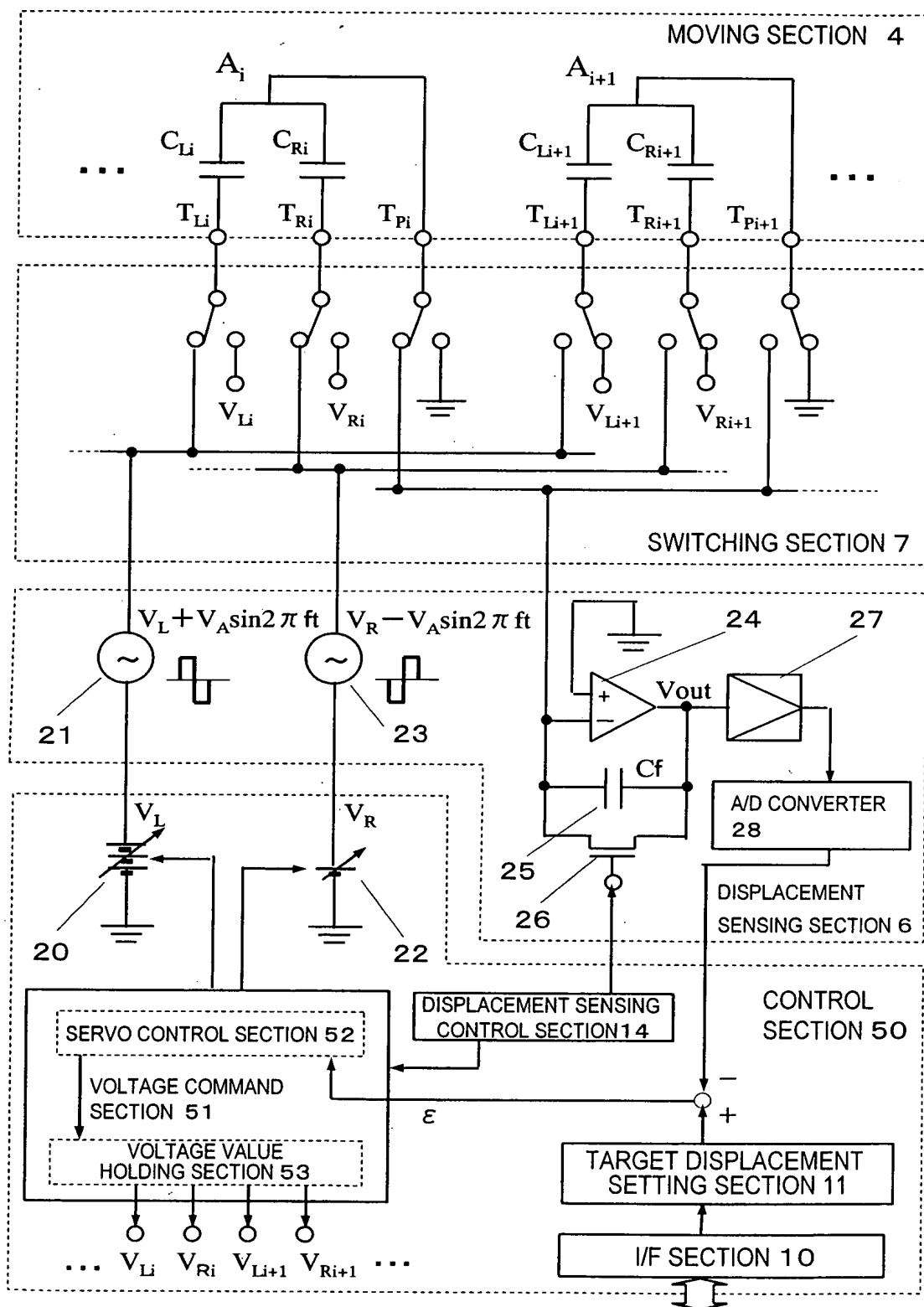


FIG.5

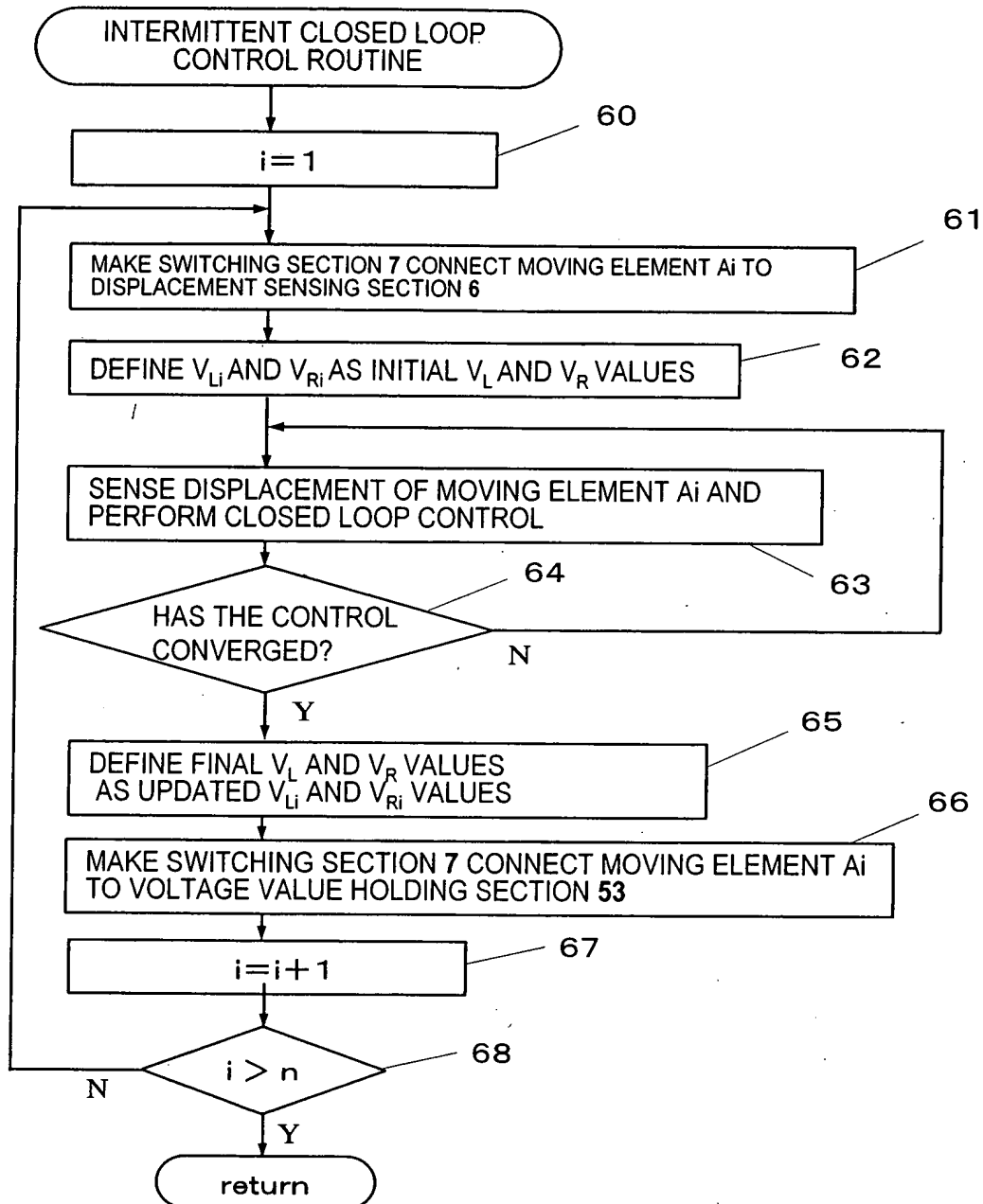


FIG. 6

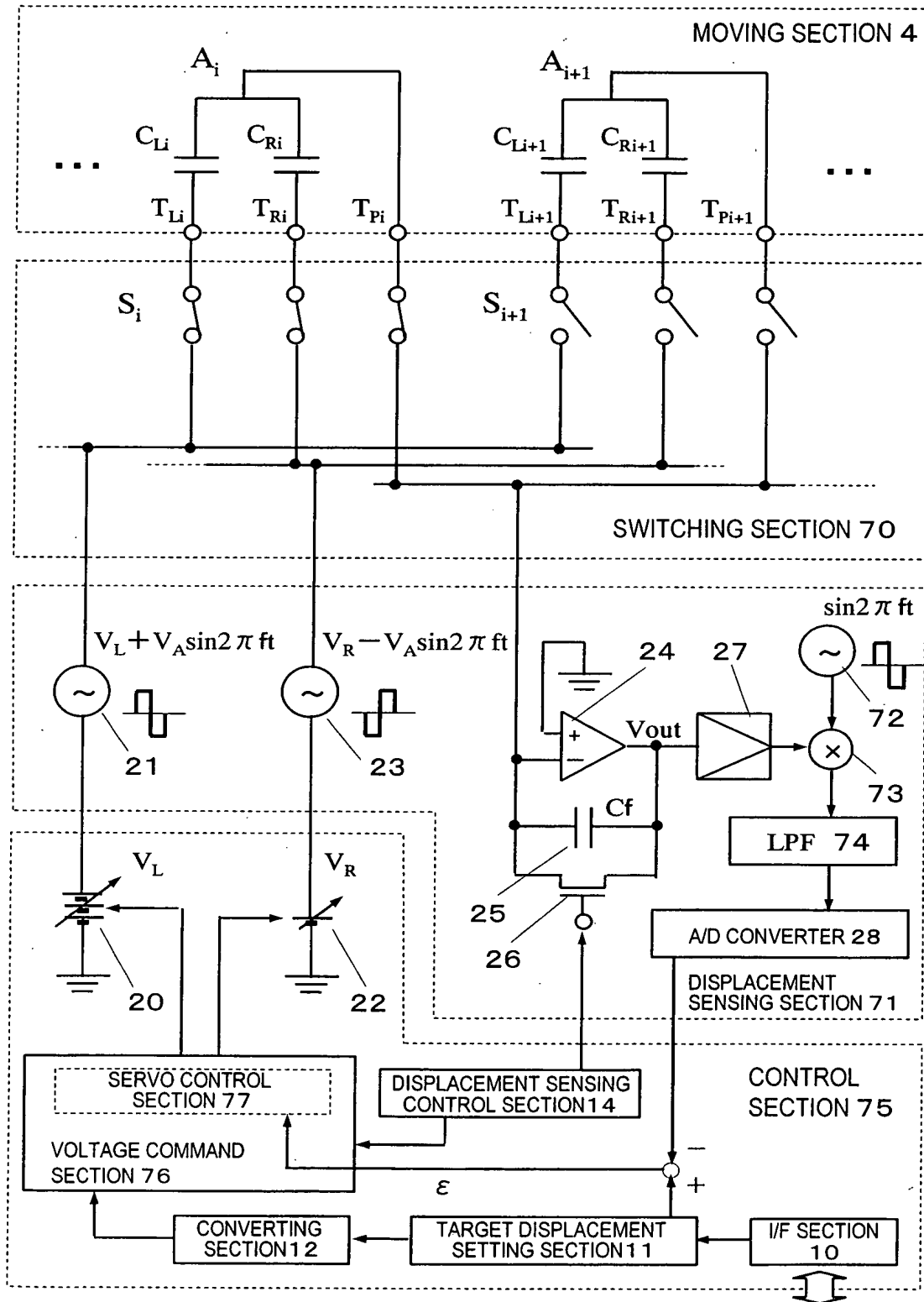


FIG. 7

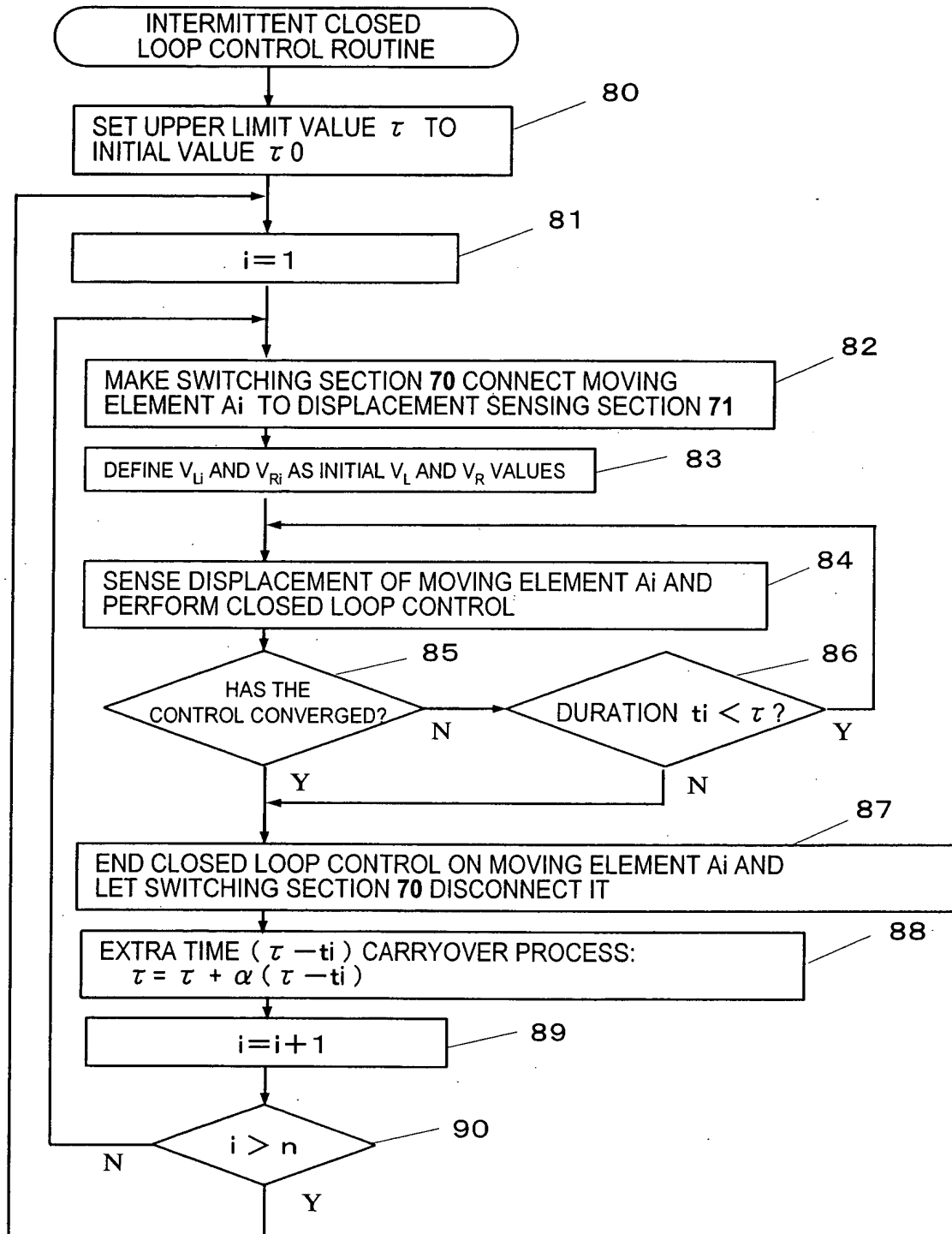


FIG. 8

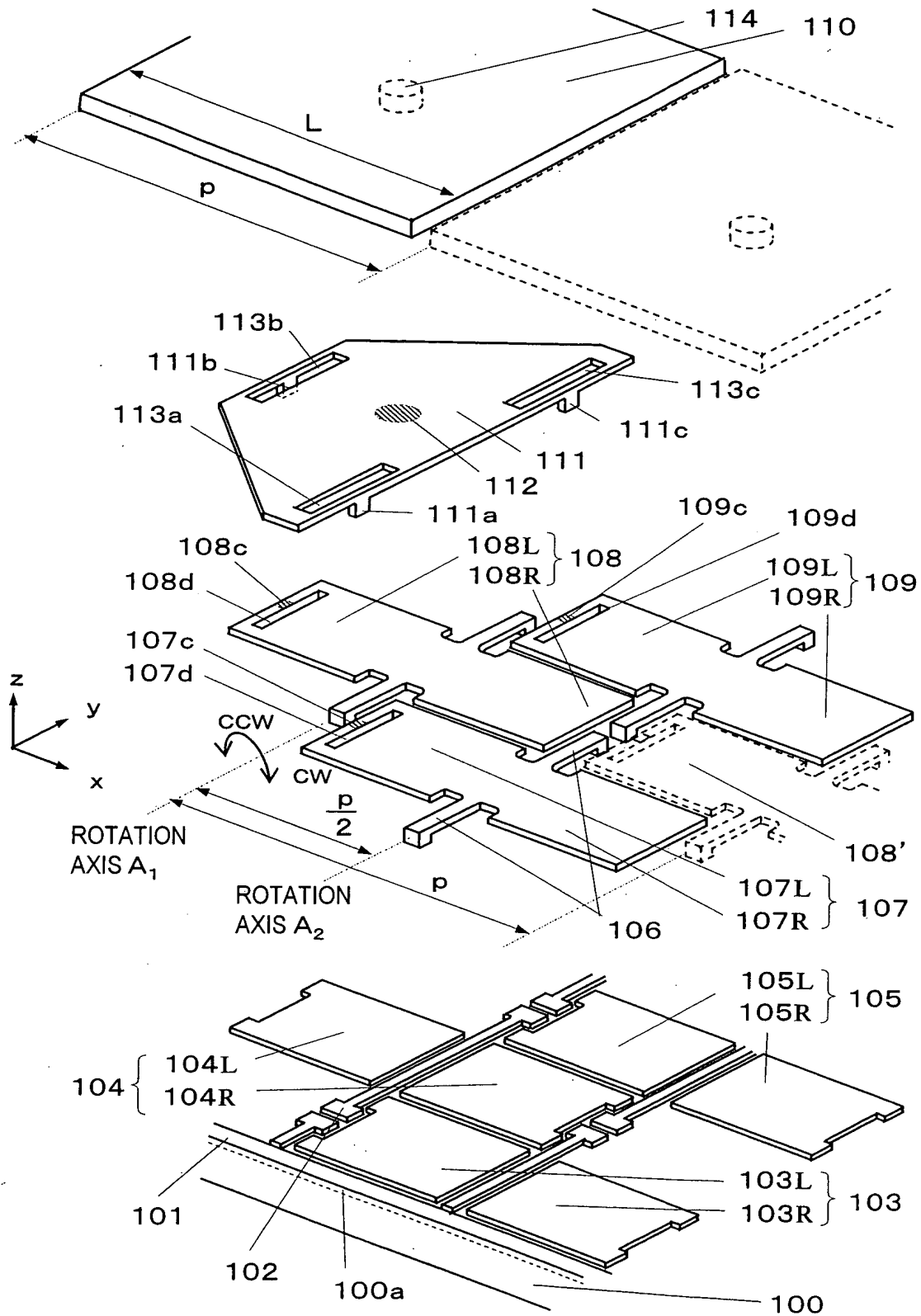


FIG.9

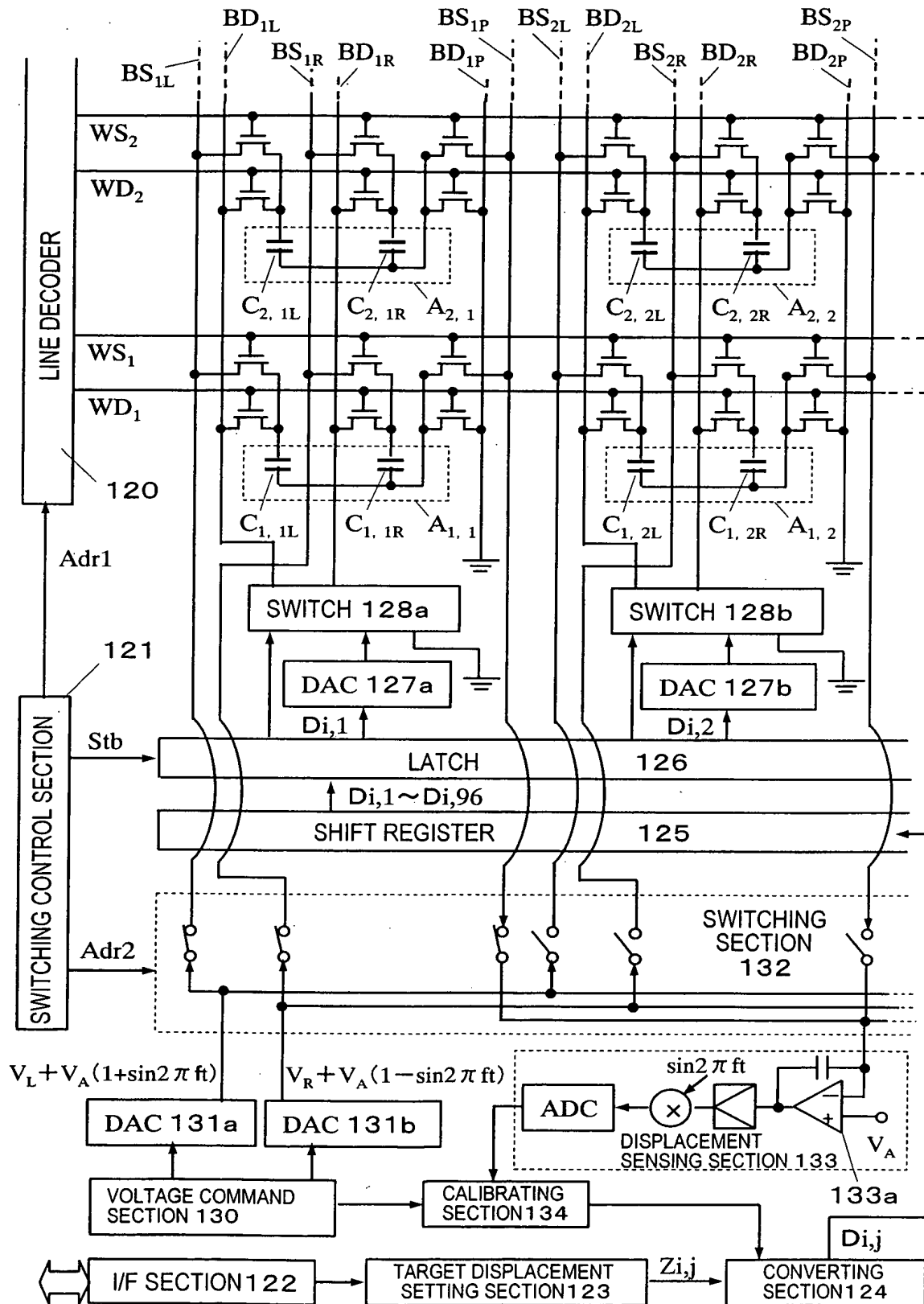


FIG. 10

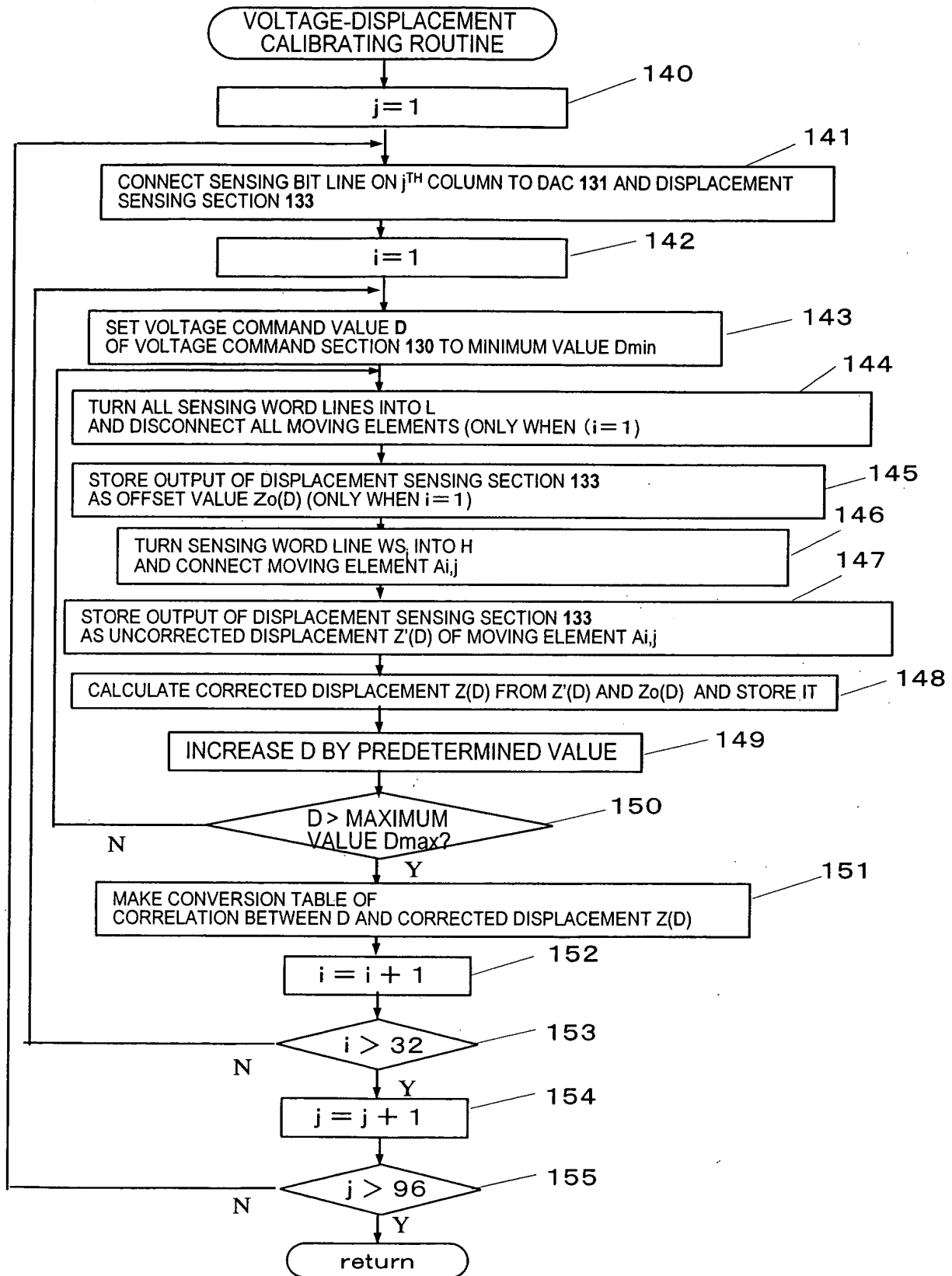
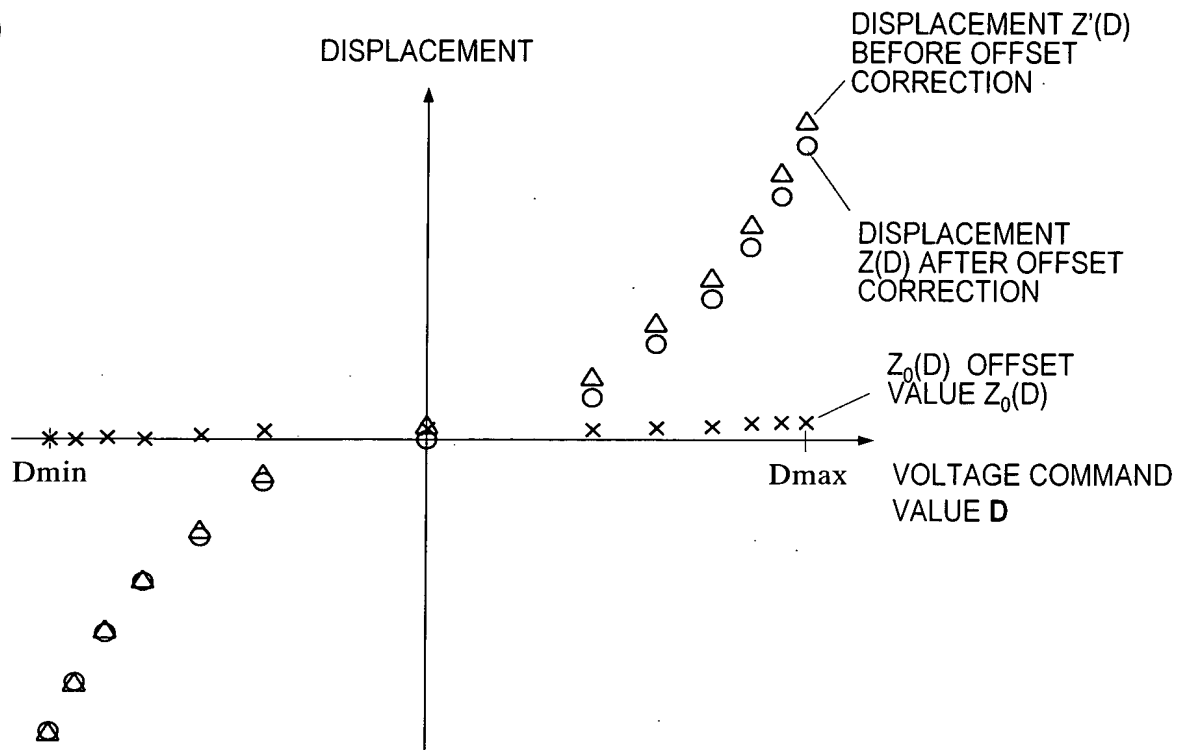


FIG. 11

(a)



(b)

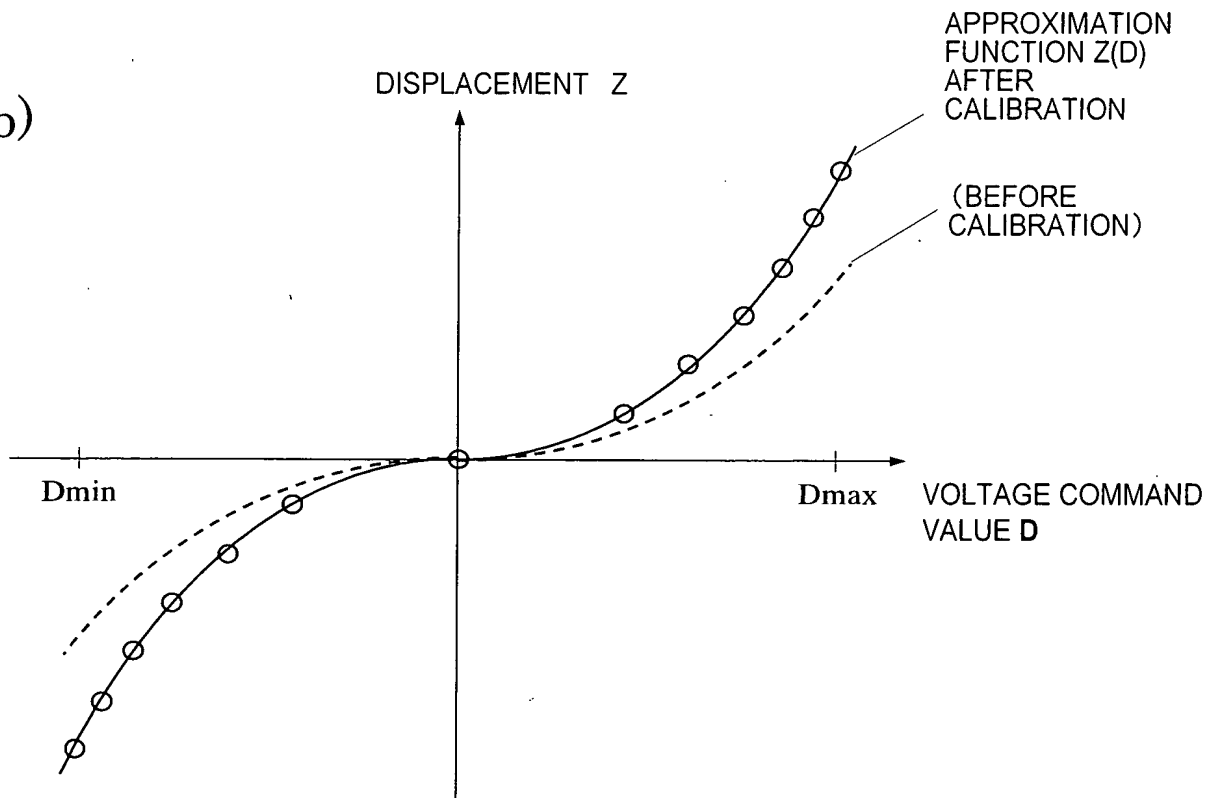


FIG.12

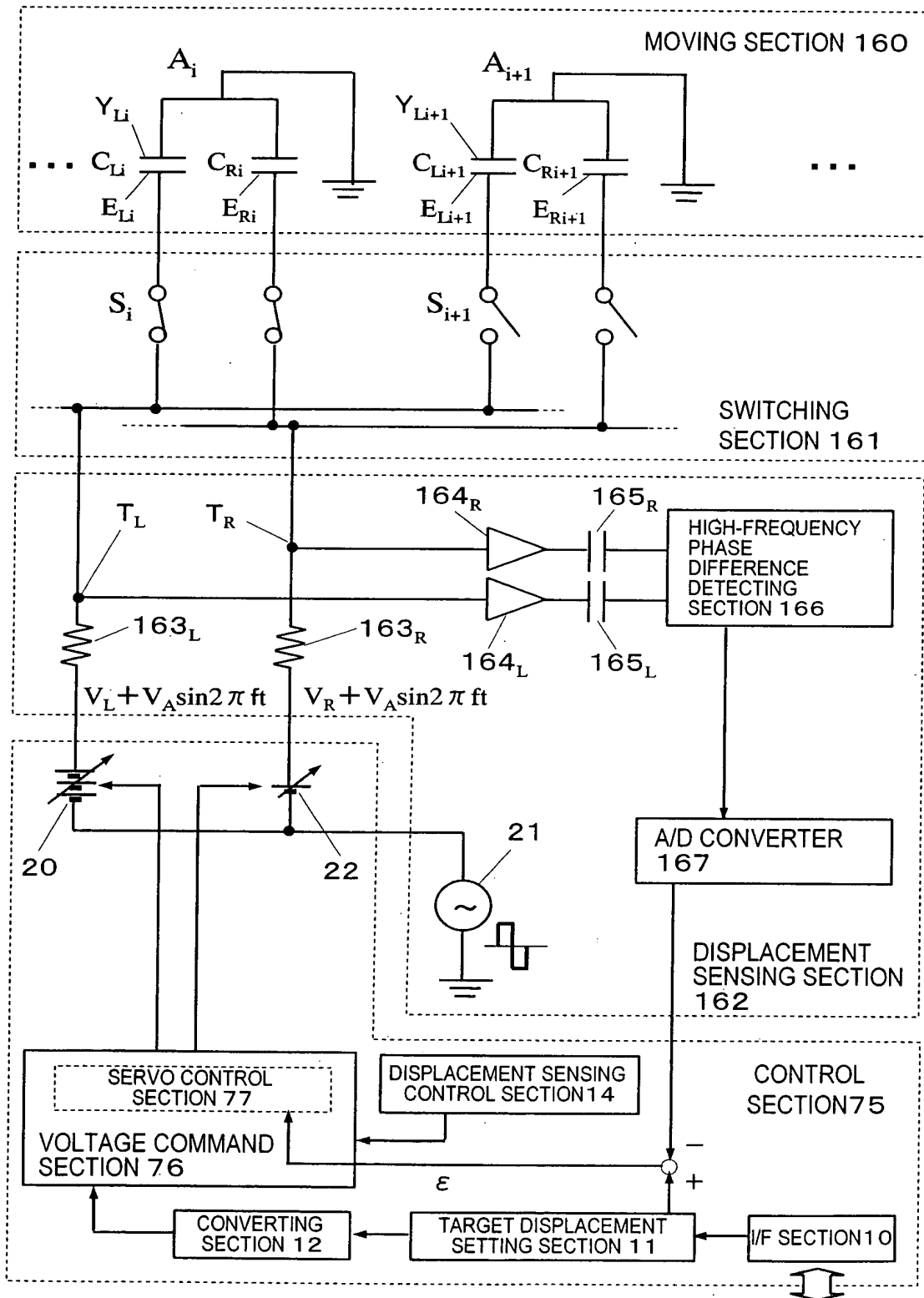


FIG.13

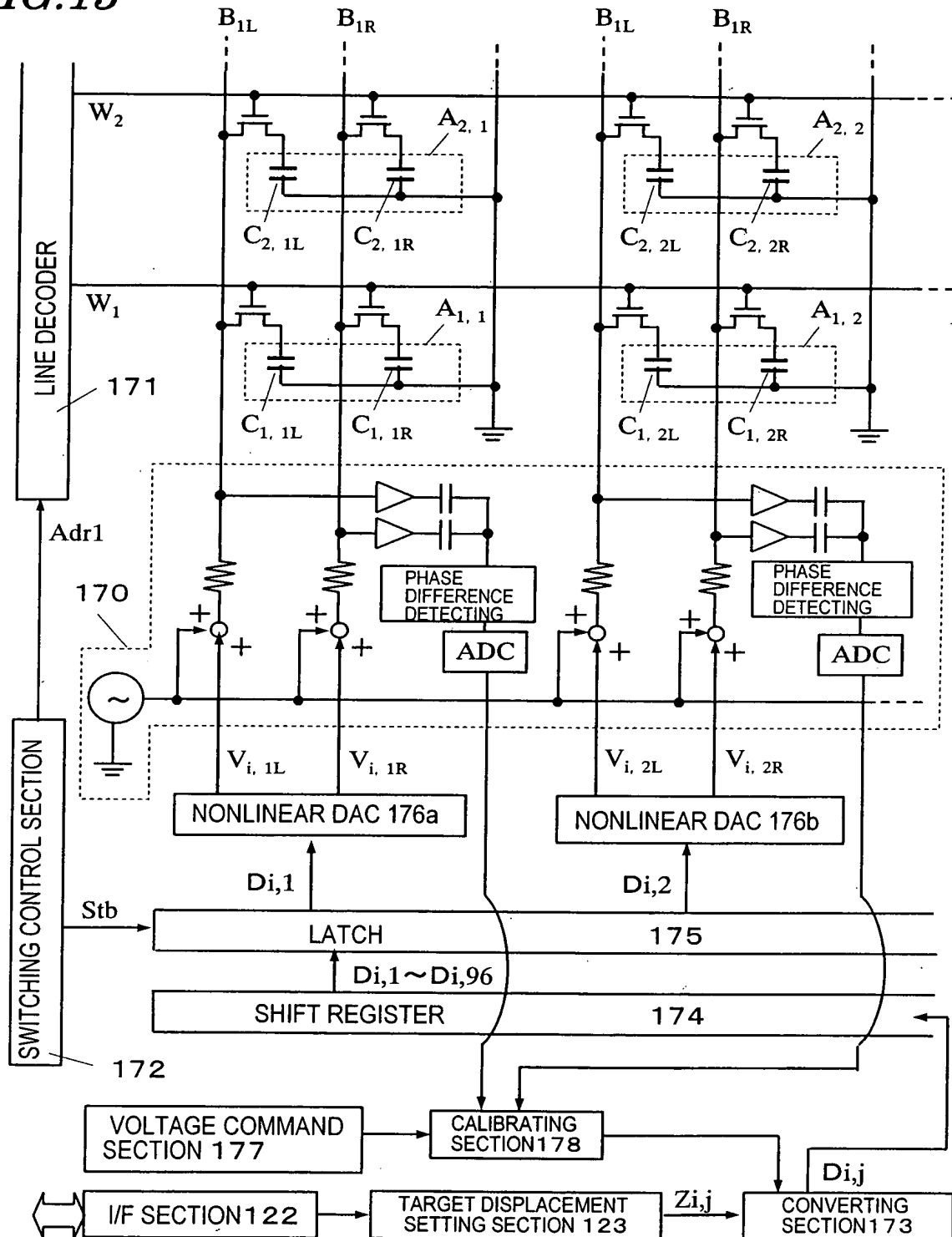
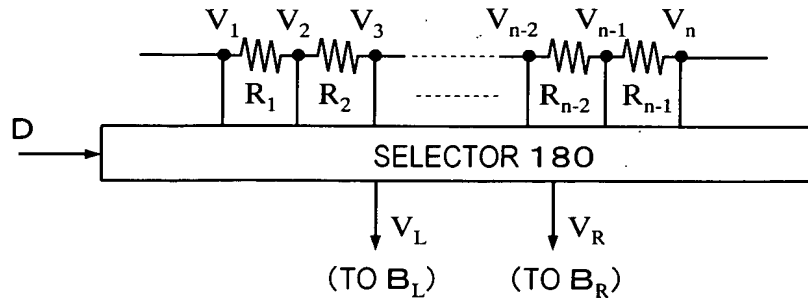


FIG. 14

(a)



(b)

